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<b>AMENDMENT</b>		Docket No. ST9-98-083
Applicant:	Stephen Lewallen	
Serial No:	09/244,291	
Filed:	February 3, 1999	
For:	METHOD AND APPARATUS FOR PROVIDING PROTOCOL INDEPENDENT NAMING AND LIFE CYCLE SERVICES IN AN OBJECT- ORIENTED SYSTEM	
Examiner:	T. Ho	
Art Unit:	2751	

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)  
The undersigned hereby certifies that this document is being placed in the United States mail  
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Commissioner for Patents, Washington, D.C. 20231 on July 23, 2002.  
*Frances M. Cunningham*  
Frances M. Cunningham

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

In response to the office communication dated April 25, 2002, please amend the  
above-identified application as follows:

**Substitute Paragraphs**

Please substitute for the paragraph beginning on page 7, line 11 the following:

A1  
Figure 1 illustrates the a computer system comprising client computers 102, 104  
and 106 intercoupled to each other and to a server 108 through a bus 105. Server 108  
may be coupled to a database 110. Each of the computers may have a system  
architecture such as an IBM PS/2®, on which the invention may be implemented. The  
exemplary computer system of Figure 1 is for descriptive purposes only. Although the  
description may refer to terms commonly used in describing particular computer

A1  
systems, such as in IBM PS/2 computer, the description and concepts equally apply to other systems, including systems having architectures dissimilar to Figure 1.

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Please substitute for the paragraph beginning on page 7, line 17 the following:

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A2  
Computer system 100 includes a central processing unit (CPU) 118, which may be implemented with a conventional microprocessor, and a random access memory (RAM) for temporary storage of information and a read only memory (ROM) for permanent storage of information, both collectively illustrated as memory 112. A memory controller is provided for controlling the RAM.

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Please substitute for the paragraph beginning on page 7, line 22 the following:

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A3  
A bus interconnects the components of each computer system. A bus controller is provided for controlling bus. An interrupt controller is used for receiving and processing various interrupt signals from the system components.

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Please substitute for the paragraph beginning on page 7, line 25 the following:

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A4  
Mass storage may be provided by diskette , CD ROM , or hard drive. Data and software may be exchanged with each computer system via removable media such as diskette and CD ROM. The diskette is insertable into a diskette drive which is, in turn, connected to the bus by a controller. Similarly, the CD ROM is insertable into CD ROM drive which is, in turn, connected to the bus by a controller. A hard disk is part of a fixed disk drive which is connected to the bus by controller.

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Please substitute for the paragraph beginning on page 8, line 3 the following:

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A5  
User input to the computer systems may be provided by a number of devices. For example, a keyboard 156 and mouse 157 are connected to the bus by controller. An audio transducer , which may act as both a microphone and a speaker, is connected to

A5

the bus by an audio controller. It will be obvious to those reasonably skilled in the art that other input devices, such as a pen and/or tabloid may be connected to the bus and an appropriate controller and software, as required. A DMA controller is provided for performing direct memory access to RAM. A visual display is generated by video controller which controls video display 170. Computer system 100 also includes a communications adapter which allows the system to be interconnected to a local area network (LAN) or a wide area network (WAN).

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